## **Amendments to the Specification**

Please replace the Abstract with the following amended Abstract:

## **ABSTRACT**

The invention relates to a- A device for detecting a demodulated signal received by a spread spectrum receiver and converted into digital samples. The device is characterized by comprising a matched filter for calculating the correlation between an incoming signal and at least one reference signal[[;]], an oscillator for generating a sampling frequency[[;]], and a sampling circuit for re-sampling the said demodulated digital sample signal at the said sampling frequency, which is such that the timing of samples of the references signals of the matched filter corresponds to the timing of a sample signal going from the sampling circuit to the matched filter[[; and]]. The device also includes a multiplier in which the sample signal is multiplied by a carrier replica generated locally before the sampling circuit or thereafter, to remove the carrier from the sample signal.

Please replace the paragraph beginning on page 10, line 11 with the following:

After the multiplication, the re-sampled and frequency-corrected samples are applied to a matched filter 4-16, which calculates their correlation with one or more reference signals. This is accomplished by loading the I and Q data streams in parallel in two shift registers in block 4-22. The signals in the data register are compared with at least one reference signal in a reference register 4-20 by correlating them with one another by means of calculation block 4-16.